

Department of Artificial Intelligence and Data Science

II Year IV Semester

4AID3-04: Microprocessor & Interfaces

Note: Each assignment of Maximum Marks 10. All question carries equal marks.

ASSIGNMENT-I

Q1. Explain pin diagram of 8085, also explain program counter, stack pointer and latch.	BLT-1	CO-1
Q2. Explain architecture of 8085 microprocessor.	BLT-1	CO-1
Q3. Classify the address bus, data bus and control bus.	BLT-2	CO-1
Q4. What is the use of flag register with example.	BLT-3	CO-1
Q5. Explain register organization of 8085.	BLT-1	CO-1

ASSIGNMENT-II

Q1. Define stack and subroutine technique with appropriate example.	BLT-2	CO-2
Q2. Briefly mention the different categories of the instruction in 8085.	BLT-2	CO-2
Q3. Explain arithmetic group of instruction. Describe each instruction of this group.	BLT-1	CO-2
Q4. Describe various addressing modes supported by the 8085 microprocessor with the help of example.	BLT-1	CO-2
Q5 Differentiate between CALL & JUMP instruction.	BLT-3	CO-2

ASSIGNMENT-III

Q1. Write the program for BCD subtraction in 8085 microprocessor.	BLT-6	CO-3
Q2. What is cod conversion? Write a program for BCD to binary conversion.	BLT-6	CO-3
Q3. Compare DAA and DAD instruction with example.	BLT-2	CO-3
Q4. Compare LHLD, SHLD, XCHG instruction with example.	BLT-2	CO-3
Q5. Explain time delay using loop within loop technique.	BLT-1	CO-3

Department of Artificial Intelligence and Data Science

II Year IV Semester

4AID3-04: Microprocessor & Interfaces

ASSIGNMENT-IV

Q1. Differentiate between Hardware and software interrupt.	BLT-2	CO-4
Q2. Explain interrupt procedure and vectored interrupt in detail.	BLT-1	CO-4
Q3. Draw the block diagram of 8251 and explain the transmitter section of 8251.	BLT-3	CO-4
Q4. Draw and explain the diagram of serial I/O communication?	BLT-3	CO-4
Q5. Explain RIM instruction in detail with example.	BLT-1	CO-4

ASSIGNMENT-V

Q1. Explain USART 8251 with block diagram?	BLT-1	CO-5
Q2. Short note on: (a) RS-232 (b) RS-422 A	BLT-1	CO-5
Q3. What is parallel interface centronics.	BLT-2	CO-5
Q4. Draw Sketch diagram of Interfacing of matrix keyboard.	BLT-3	CO-5
Q5. Interface LCD display interfacing with microprocessor 8085.	BLT-6	CO-5

*BLT: BLT shows the **Bloom's taxonomy** levels